

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known

Application Number	10/647,688
Filing Date	August 25, 2003
First Named Inventor	S. Brandon Keller
An Unit	2805
Examiner Name	Whitmore
Attorney Docket Number	100111238-1

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
AW		US-2002/010901 A1			
		US- 6,185,722 B1			
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
AW		JP 07334532 A	12/22/1995	Mitsubishi Denki Semiconductor		

Examiner Signature	AW	Date Considered	6/2/05
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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PATENT APPLICATION

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LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

100111238-1

APPLICATION NO.

10/647,688

CONFIRMATION NO.

APPLICANT

S. Brandon Keller, et al.

FILING DATE

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GROUP

2825

REFERENCE DESIGNATION

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EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
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	1L				
	1M				
	1N				
	1O				
	1P				

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

AW	1Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,682, Entitled: Method And Program Product For Determining Nets Requiring Detailed Electromigration And Self Heating Analysis In A Digital Integrated Circuit; Filed November 12, 2003
716 but is in 2184 716	1R	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,698, Entitled: Method And Program Product For Performing Self-Heating Analysis In A Digital Integrated Circuit Through A Single Cycle Transient Simulation; Filed November 12, 2003
AW	1S	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,376, Entitled: Method And Program For Visual Display and One-Click Repair Of Electromigration And Self Heating Design-Rule Violations In A Digital Integrated Circuit Layout Database; Filed November 12, 2003

EXAMINER

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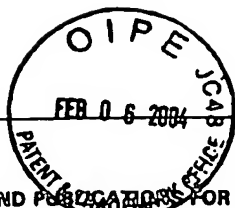
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	2L				
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

2Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,501, Entitled: Method And Program Product For Performing Electromigration Analysis In A Digital Integrated Circuit By Converting A Netlist To A DC Model And Performing DC Analysis Of The DC Model; Filed November 12, 2003
2R	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,526, Entitled: Method And Program Product For Performing A Worst Case Electromigration And Self Heating Analysis In A Digital Integrated Circuit With Worst-Case Superposition Of Partial Currents; Filed November 12, 2003
2S	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,692, Entitled: Method And Program Product For Performing Electromigration Analysis In A Digital Integrated Circuit Through A Single Cycle Transient Simulation; Filed November 12, 2003

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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

AW	3Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,508, Entitled: Method And Program Product For Performing Self-Heating Analysis In A Digital Integrated Circuit Layout Database by Substituting Resistive Models For Active Devices; Filed November 12, 2003
AW	3R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655516US; Entitled: Method And Program Product For Determining Worst Case Currents In A Digital Integrated Circuit Through Worst-Case Superposition Of Partial Currents; Docket No.: 200300123-1; Filed January 30, 2004 10/769, 675
AW	3S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERTS, George H.; U.S. Patent Application filed under EV210655564US; Docket: 200308901-1; Entitled: Systems And Methods For Re-Using Circuit Design Analysis Results; Filed January 30, 2004 10/769, 702

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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

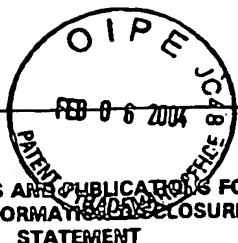
AW	4Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655581US; Docket No.: 200308899-1; Entitled: System And Method For Determining Detail Of Analysis In A Circuit Design; Filed January 30, 2004 10/769,687
AW	4R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655595US; Docket No.: 200308898-1; Entitled: Systems And Methods That Identify Equivalent Instantiation-Specific Configuration Information For Analysis Tools; Filed January 30, 2004 10/769,495
AW	4S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H. & STEVENS, Scott Alan; U.S. Patent Application filed under EV210655520US; Entitled: System And Method To Limit Analyzed Current Flow In A Circuit Design; Filed January 30, 2004 10/769,682

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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

5Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655533US; Docket: 200310172-1; Entitled: System And Method For Processing Configuration Information; Filed January 30, 2004 10/768,442
5R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655555US; Docket: 200310170-1; Entitled: System And Method For Balancing Run-Time And Result Accuracy In A Circuit Design Analysis Tool; Filed January 30, 2004 10/769,683
5S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655578US; Docket: 200308900-1; Entitled: System And Method For Indicating Logic State Combinations Used During Circuit Design Analysis; Filed January 30, 2004 10/769,676

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<i>AW</i>	6Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210855547US; Docket: 200310171-1; Entitled: System And Method For Determining Control Signal Combinations For Use During Simulation Of A Stage Of A Circuit Design; Filed January 30, 2004 <i>101769,673</i>
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	6S	

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